

Instructions: This is a closed book, closed note exam. Calculators are not permitted. If you have a question, raise your hand and I will come to you. Please work the exam in pencil and do not separate the pages of the exam. For maximum credit, show your work.

Good Luck!

Your Name (*please print*) _____

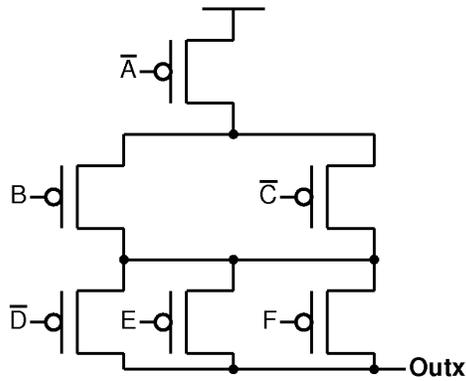
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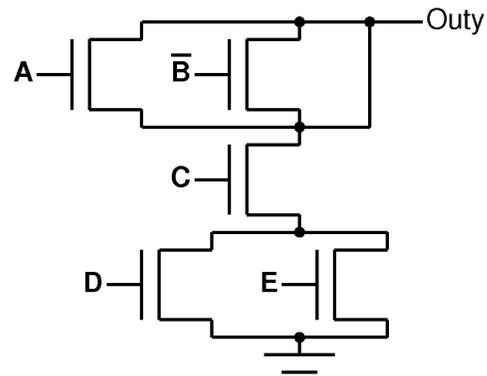
Problem 1 (3 parts, 24 points)

Switch-level Design

The three parts below contain (A) a pull up network, (B) a pull down network, and (C) an expression to be implemented. For (A) and (B), complete the missing complementary switching networks so the circuit contains no floats or shorts and write the Boolean expression computed by the completed circuit. For (C), design the entire switching network. Assume the inputs and their complements are available.



(A)



(B)

OUT_x = _____

OUT_y = _____

(C)

OUT_z = $A \cdot C + B \cdot (\bar{D} + \bar{E})$

Problem 2 (2 parts, 28 points)

Mixed Logic Reengineering

For the following expressions, implement the Boolean expression using the specified gate type. **Use correct mixed-logic notation. Do not simplify the expression.** You may use multi-input gates. Minimize the total transistors (switches) required. When possible, use common subexpressions to reduce gate counts. Also determine the number of switches used in each implementation.

Part A (14 points) Implement $\overline{\overline{A \cdot (B + \overline{C}) \cdot (D + E) + (D + E)}}$ using only **NOR** and **NOT** gates.

switches = _____

Part B (14 points) Implement $\overline{\overline{\overline{A + (B \cdot \overline{C} + D) + \overline{E} \cdot F}}}$ using only **AND** and **NOT** gates.

switches = _____

Problem 3 (2 parts, 18 points)

Boolean Algebra

Part A (10 points) Transform each of the following Boolean expressions to a form where they are ready for switch level implementation (i.e., there should only be bars over input variables, not over operations). The behavior of the expression should remain unchanged. **Do not implement.**

$$Out_x = \overline{\overline{A \cdot \overline{B}} + C \cdot \overline{D} \cdot \overline{E} + F + G}$$

$$Out_y = \overline{\overline{R \cdot S} + M \cdot \overline{N} \cdot P + \overline{L} \cdot \overline{K} \cdot J}$$

Part B (8 points) For the behavior described by this truth table, (A) write the sum of products expression using minterms and (B) write the product of sums expression using maxterms.

A	B	C	Out
0	0	0	1
1	0	0	1
0	1	0	0
1	1	0	1
0	0	1	0
1	0	1	0
0	1	1	1
1	1	1	0

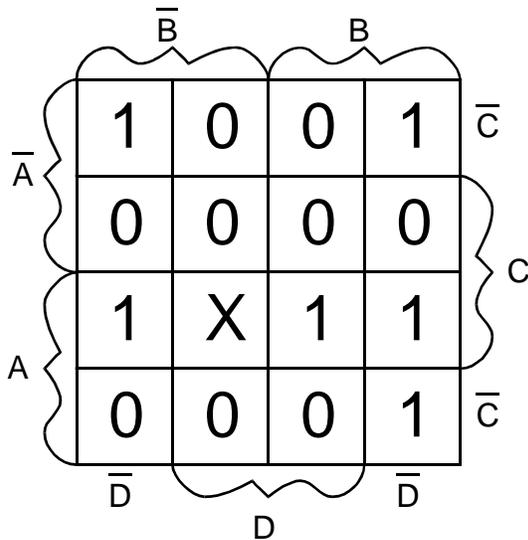
(A) SOP minterm expression

(B) POS maxterm expression

Problem 4 (2 parts, 30 points)

Karnaugh Maps

Part A (15 points) Given the following Karnaugh Map, circle and list all the prime implicants for a **sum-of-products (SOP)** expression, indicating which are essential. Derive the simplified SOP expression.

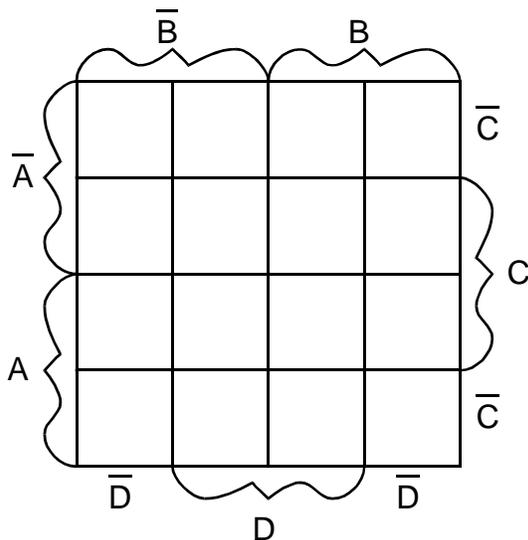


prime implicants	essential?	
	yes	no
_____	<input type="checkbox"/>	<input type="checkbox"/>
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simplified SOP expression _____

Part B (15 points) For the following expression, derive a simplified **product of sums** expression using a Karnaugh Map. Circle and list all the prime implicants, indicating which are essential.

$$Out = (A + B + C) \cdot (\overline{A} + \overline{B} + C + D) \cdot (A + C + D) \cdot (A + B + \overline{C} + \overline{D}) \cdot (\overline{A} + B + C)$$



prime implicants	essential?	
	yes	no
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simplified POS expression _____